

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1467	(257/401).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/16 08:49
L2	18	L1 and (checker adj board checkerboard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/16 08:49
L3	25	(lateral adj2 (DMOS MOSFET MOS) LDMOS) and (checker adj board checkerboard)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 08:49
L4	14	L2 not L3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/05/16 08:49
L5	369	(257/E29.026).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/16 08:50
L6	368	(257/E29.027).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/16 08:50
L7	451	(257/342).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/16 08:50
L8	405	(257/343).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/16 08:50
L9	620	(257/339).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/16 08:50
L10	896	(257/341).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/16 08:50
L11	815	(257/409).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/05/16 08:51
L12	179	(lateral adj2 (DMOS MOSFET MOS) LDMOS) and hot adj (electron hole carrier)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 08:51
L13	109	L9 and (LDMOS lateral adj2 (DMOS MOS MOSFET))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 08:51
L14	95	L11 and (LDMOS lateral adj2 (DMOS MOS MOSFET))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 08:51